APR 2 5 2002 CS

ØRNEY DOCKET NO.: M.B. BAILLIE 2-2

#19/B #19/B PATENT 11/4/2 Surles

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Matthew B. Baillie, et al.

Serial No.:

09/138,146

Filed:

August 21, 1998

Title:

INTEGRATED CIRCUIT CARRIER AND METHOD OF

MANUFACTURING AND INTEGRATED CIRCUIT

Grp./A.U.:

2815

Examiner:

S. Clark ·

Honorable Commissioner for Patents Washington, D.C. 20231

hereby certify that this correspondence is being deposited with the United States Postal Service as first class mall in his envelope addressed to: Commissioner for Paterin, Washington, D.C.

(Printed or typed name of person sillaing the certific

(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed March 12, 2002, please amend the aboveidentified application as follows:

IN THE CLAIMS:

- (1) Please rewrite Claim 1 as follows:
- 1. (Twice Amended) A chip carrier comprising:

a base;

an inner well extending along a length of a periphery of the base; and an outer well extending along a length of a periphery of the inner well.